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Vickie D. Wall

Appl No. : 10/621,005 Confirmation No. 9113
Applicant : Janardhanan S. Ajit
Filed : July 16, 2003
Title : SUB-MICRON HIGH INPUT VOLTAGE TOLERANT INPUT OUTPUT
(I/O) CIRCUIT

TC/A.U. : 2816
Examiner : Tuan Thieu Lam

Docket No. : 50716/PAN/B600
Customer No. : 23363

SUPPLEMENTAL AMENDMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Post Office Box 7068
Pasadena, CA 91109-7068
December 2, 2004

Commissioner:


Initially, the Applicant would like to express its gratitude toward the Examiner for taking time to discuss the present application by telephone on November 29, 2004, prior to making this submission. The amendments herein reflect the subject matter discussed by telephone with the Examiner.

Specifically, as discussed with the Examiner on November 29, 2004, the Applicant has amended FIG. 17 to correct certain minor typographical mistakes. No new matter has been added. A marked up copy of the changes made are enclosed for the Examiner's convenience, in addition to a complete set of replacement drawings.

Appln No. 10/621,005
Amdt date December 2, 2004
Reply to Office action of

The Applicant respectfully requests entry of this amendment
prior to examination.

Respectfully submitted,
CHRISTIE, PARKER & HALE, LLP

By 
Peter C. Hsueh
Reg. No. 45,574
626/795-9900

PCH/vdw

Enclosures Replacement Drawings (23 sheets)
 Annotated FIG. 17

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